

FIG. 1 is a block diagram of a digital signal processing system.

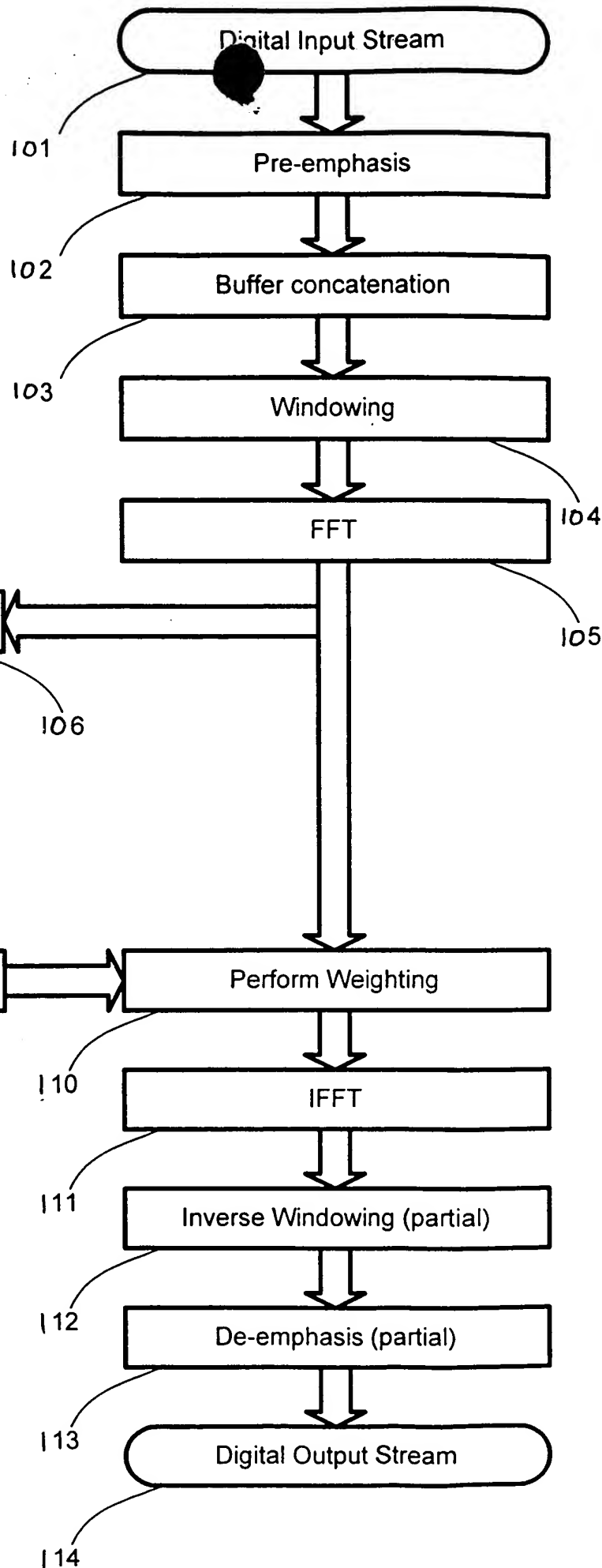


FIGURE 1

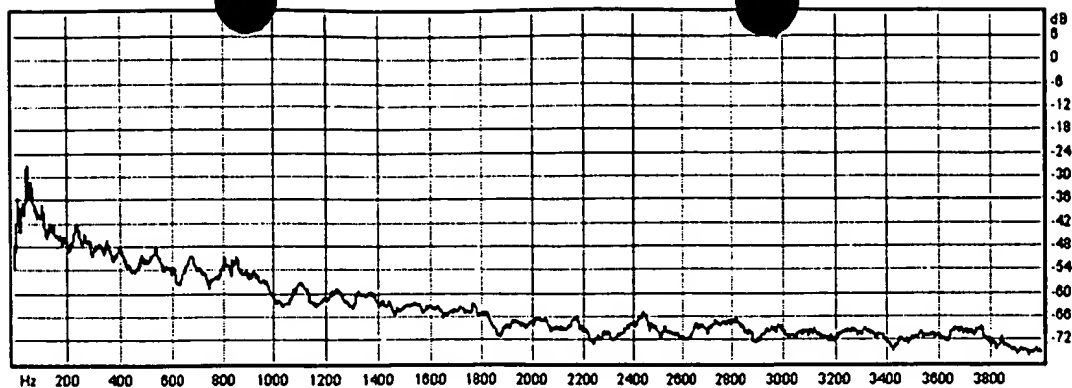


Fig 2a: Frequency plot of large sample of speech and noise.

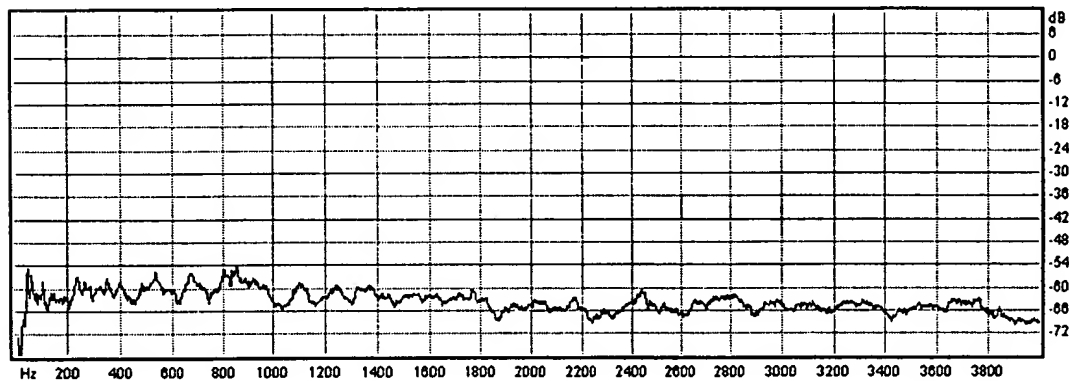


Fig 2b: Frequency plot of large sample of speech and noise after emphasis function has been applied.

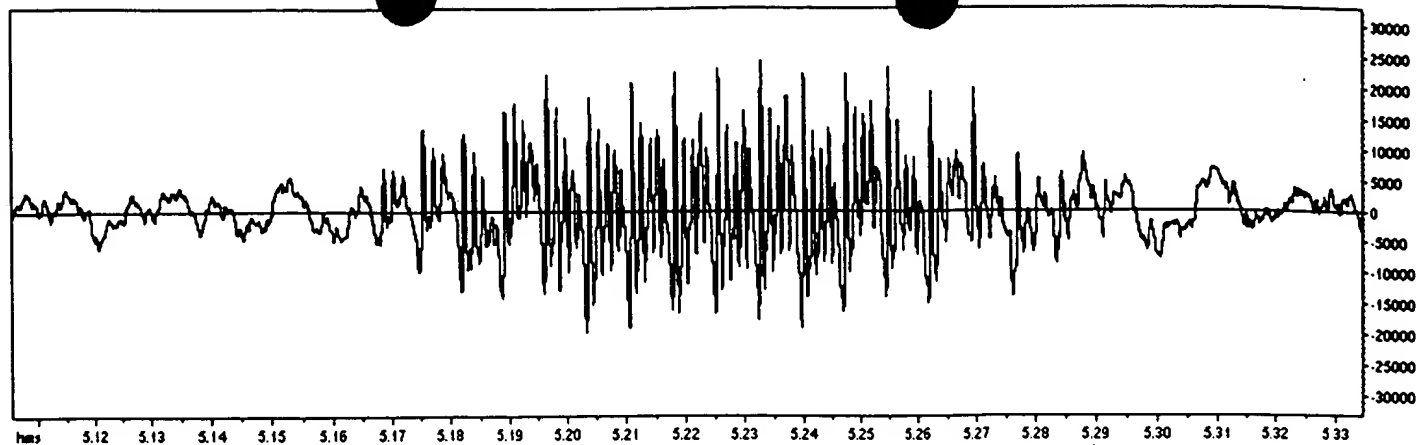


Fig 3a: Time domain plot of short sample of speech and noise.

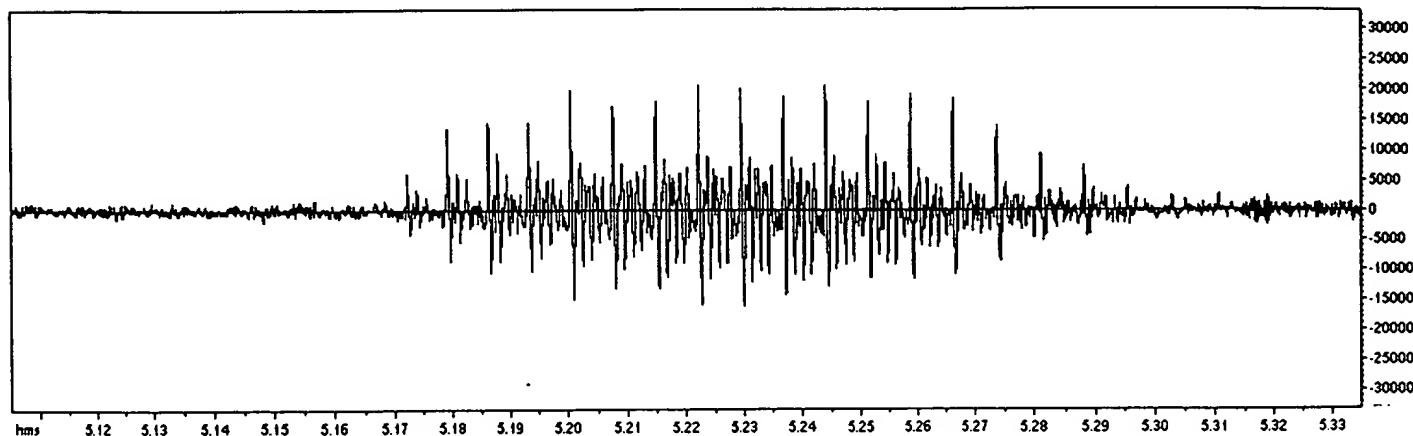
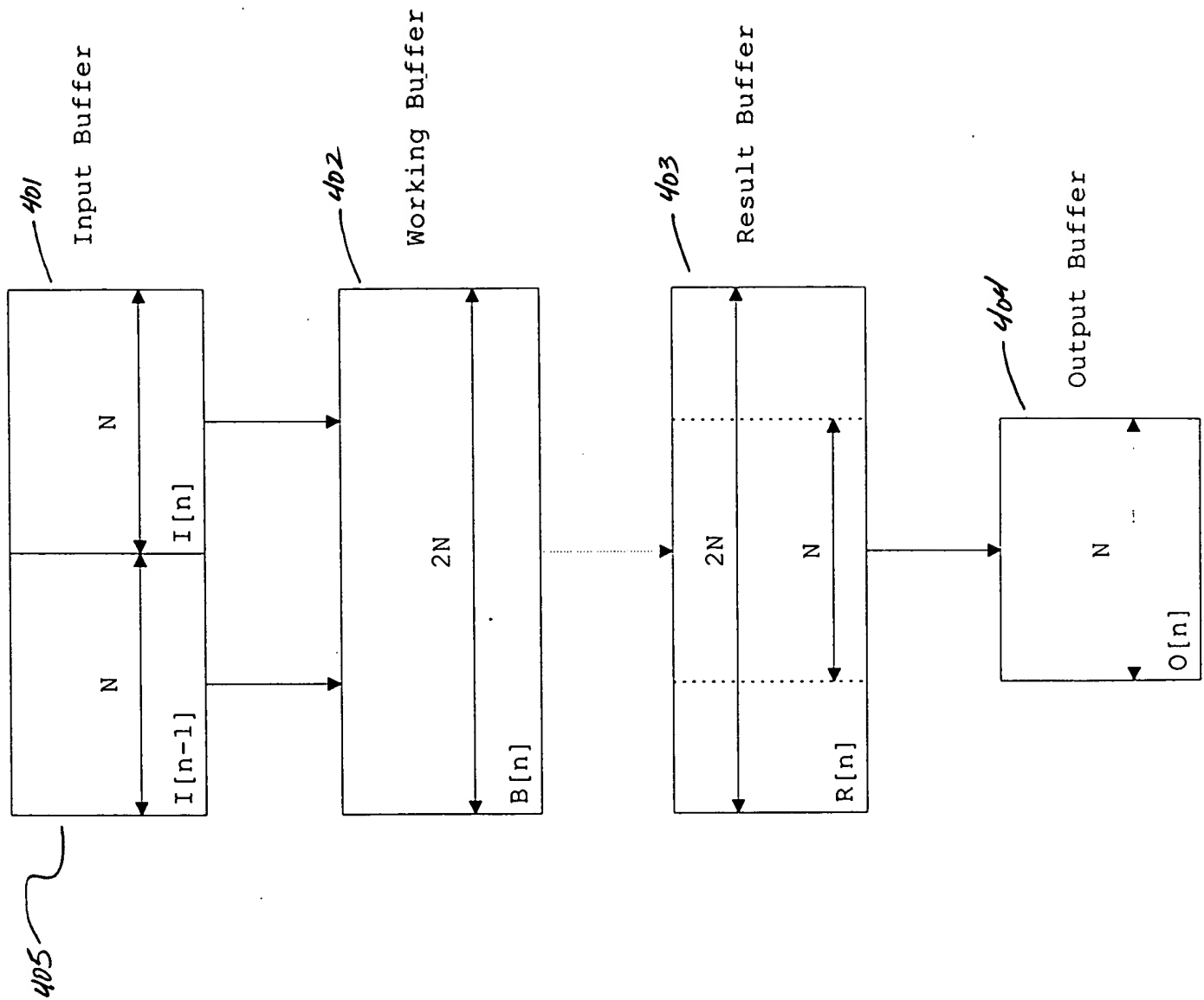


Fig 3b: Time domain plot of short sample of speech and noise after emphasis function has been applied.

FIG. 4 is a block diagram of a data processing system 400, in accordance with the present invention, for processing data in a buffer.



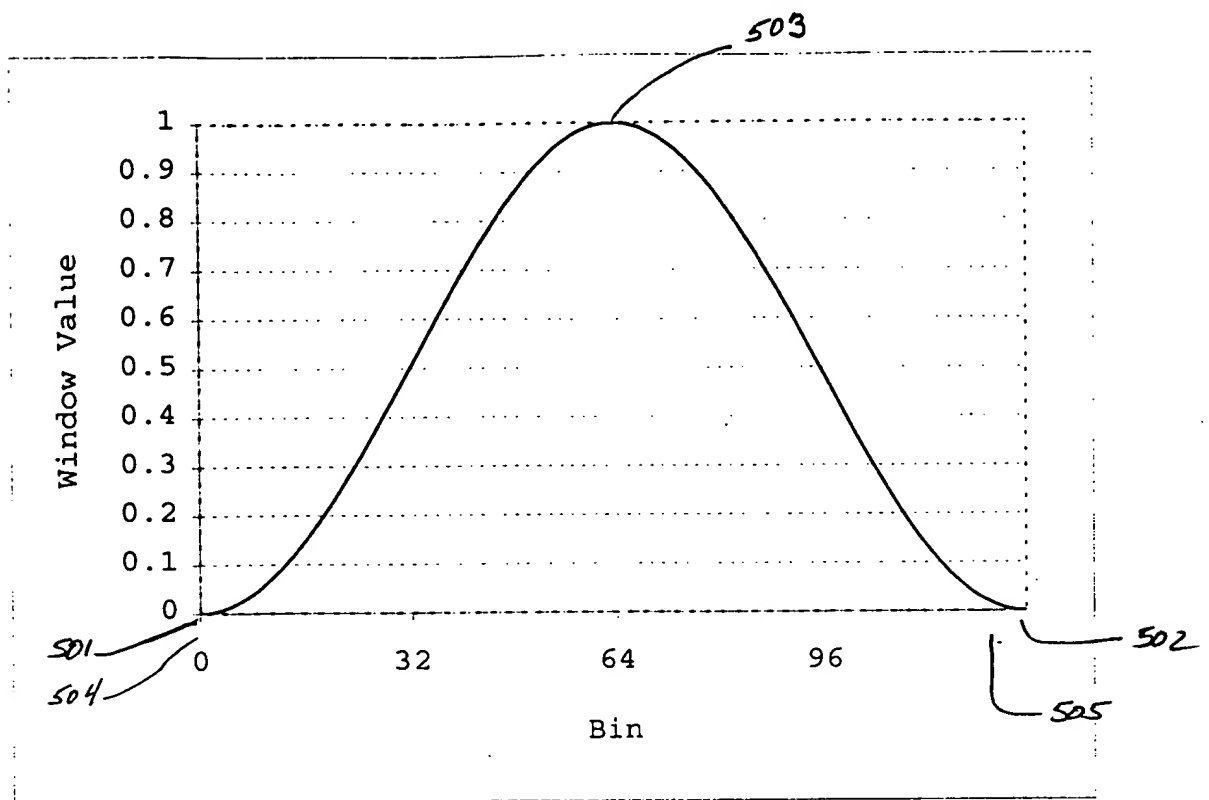


Fig 5a: Hanning Window Function

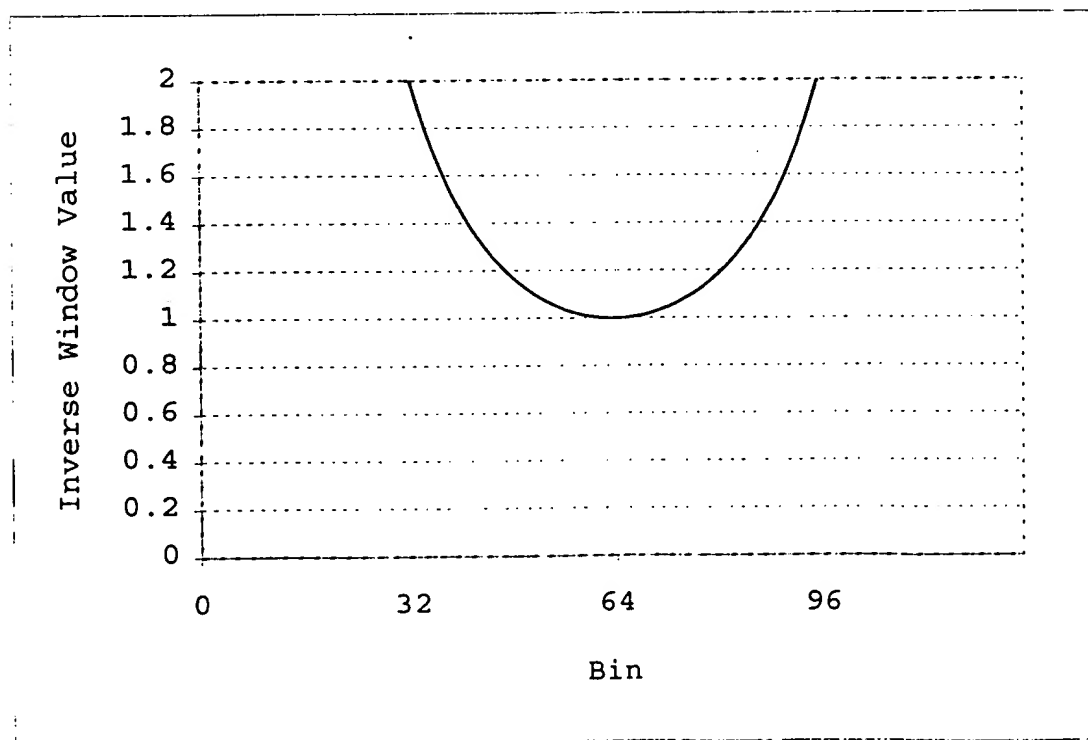


Fig 5b: Inverse Hanning Window Function

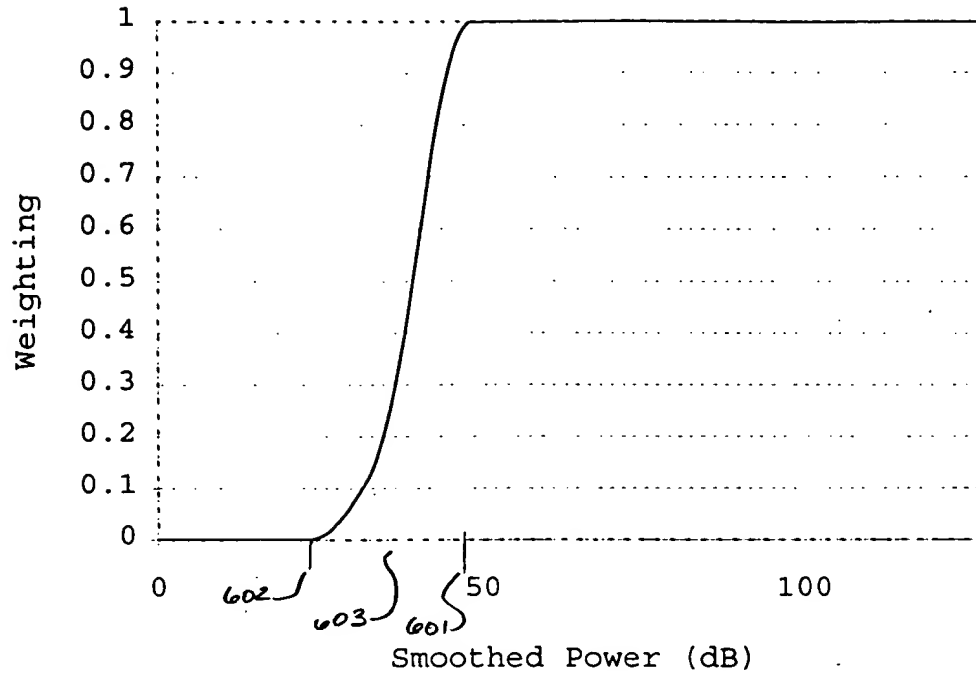
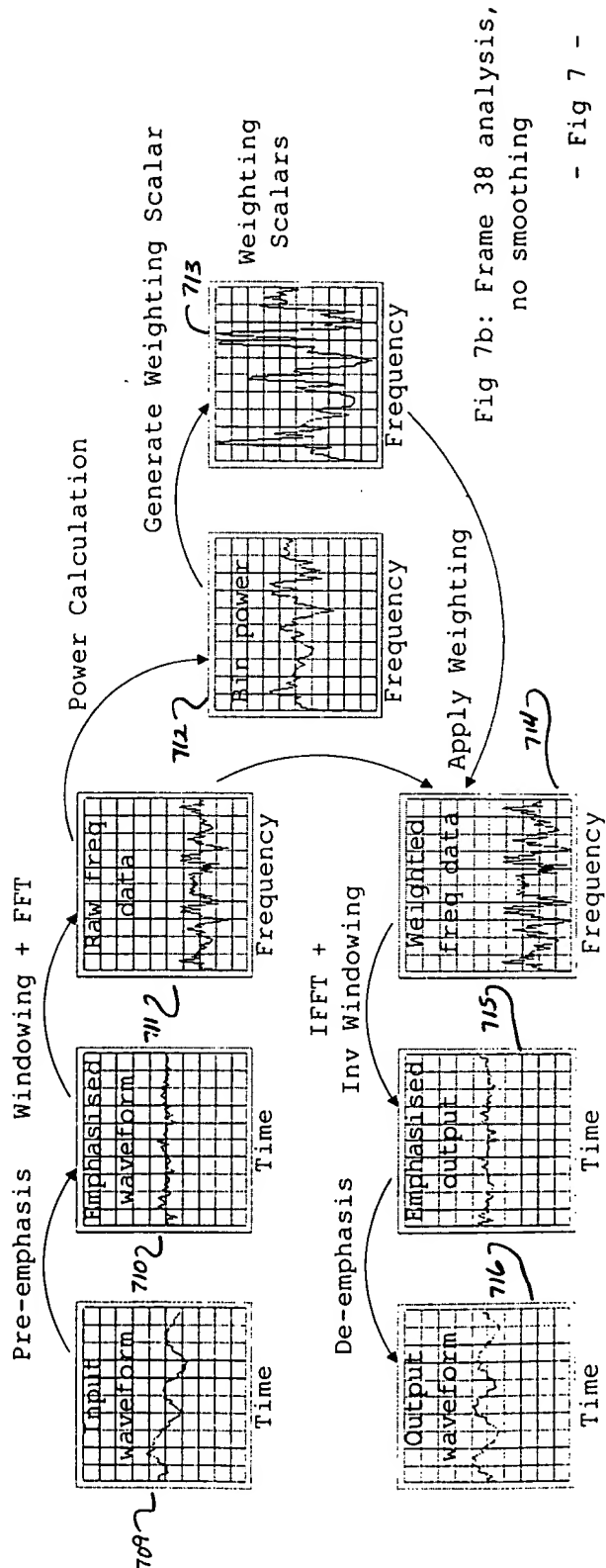
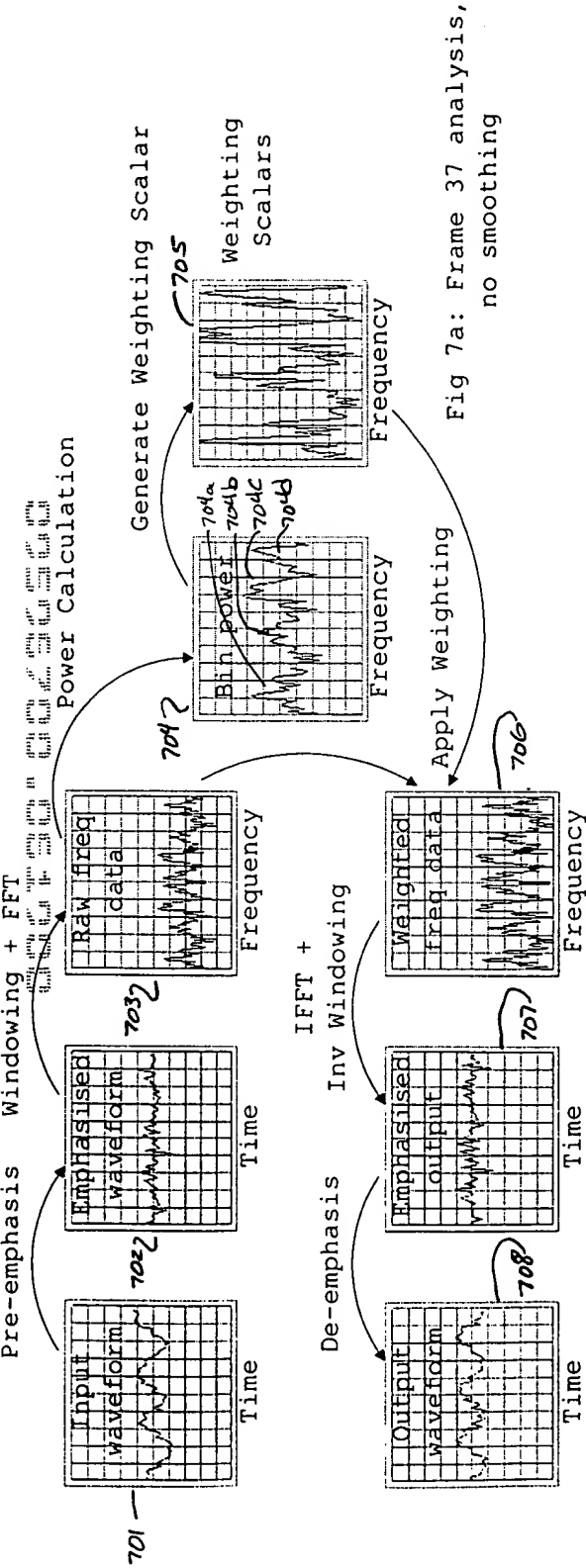
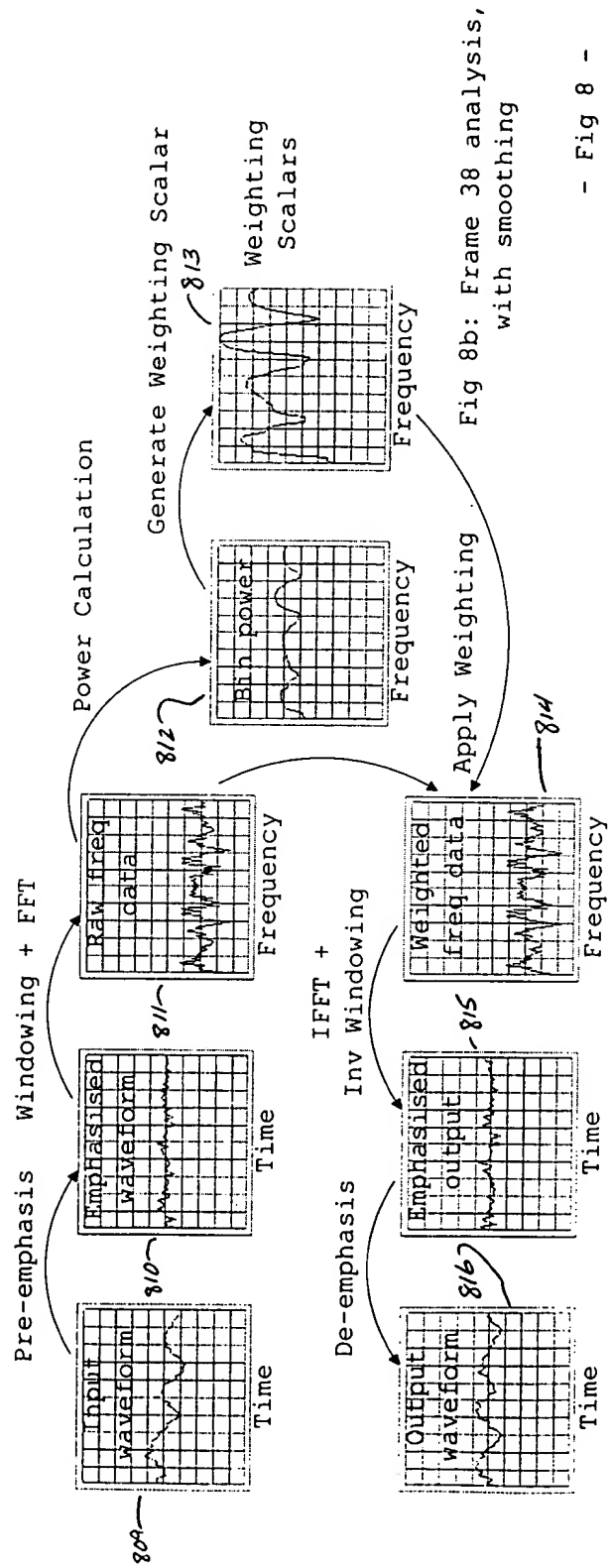
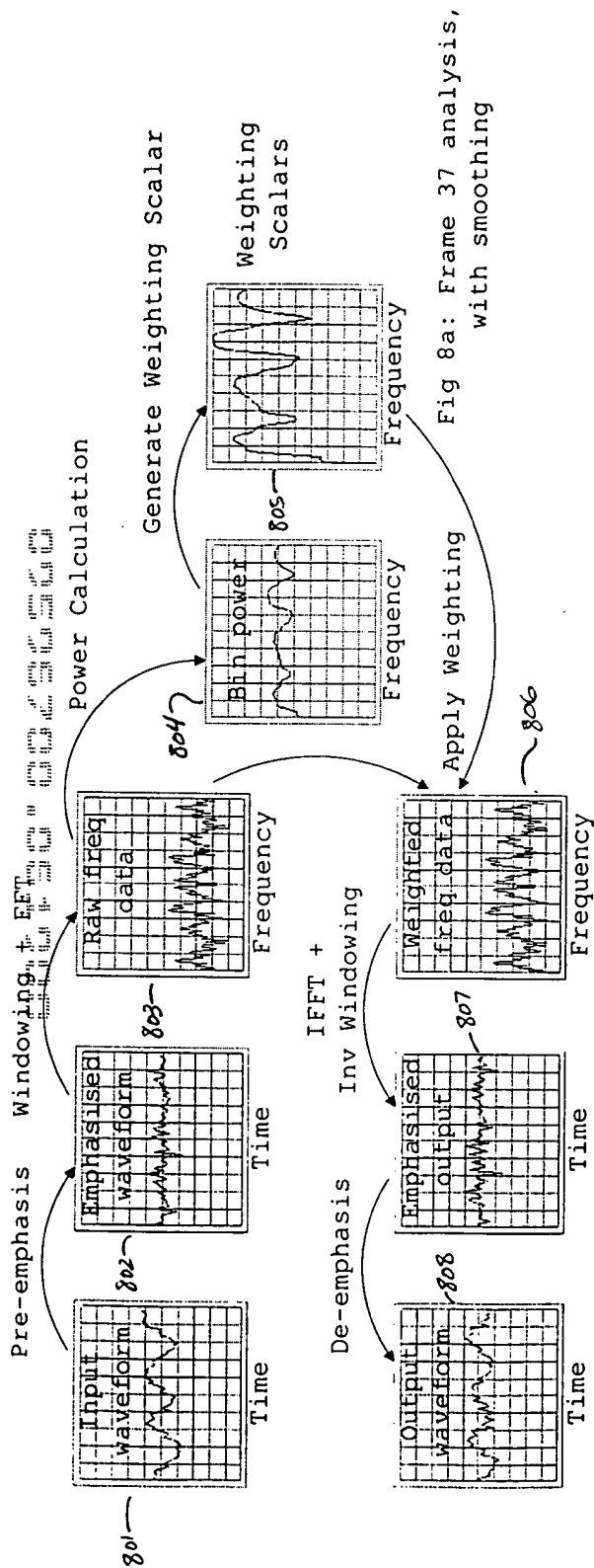
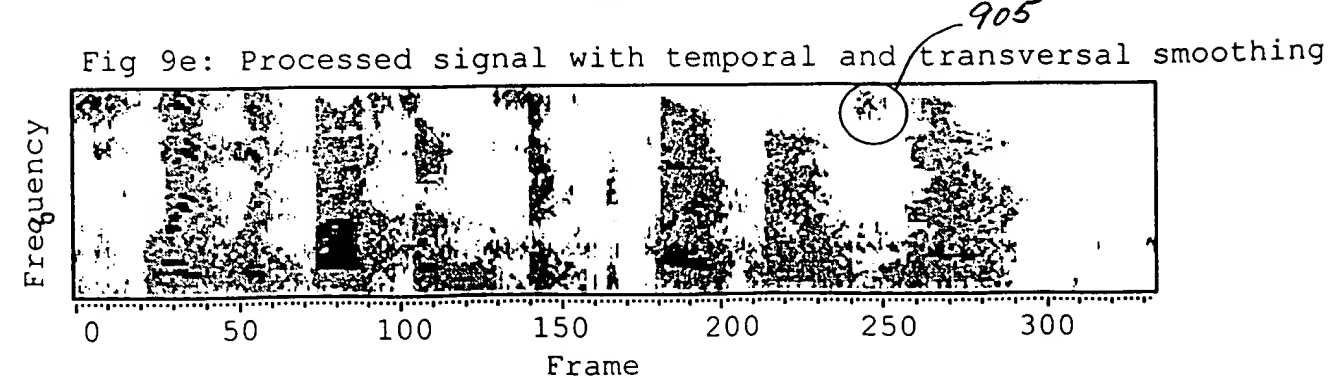
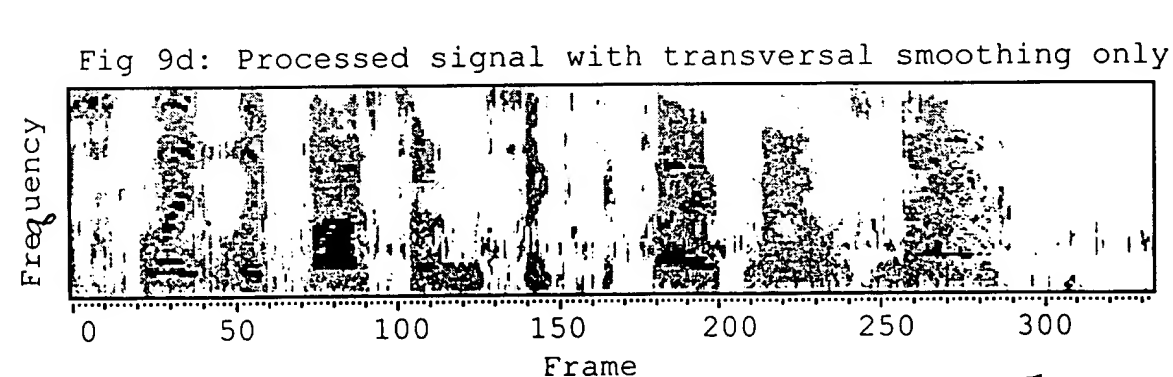
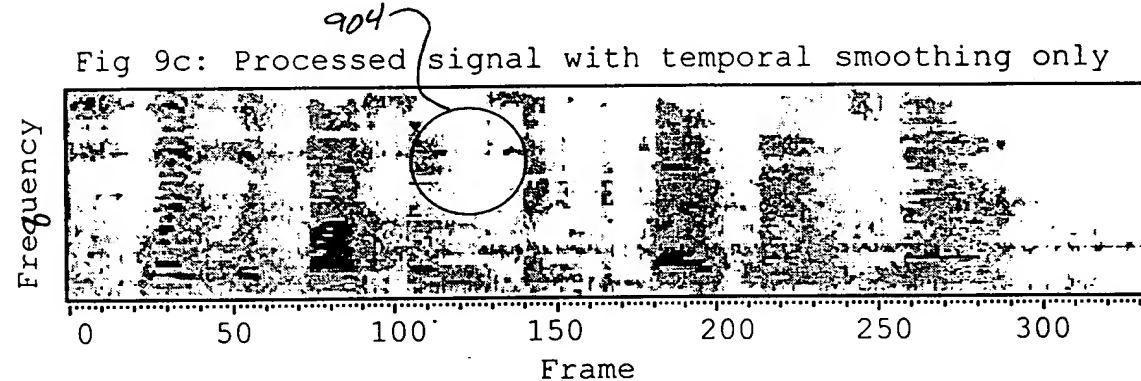
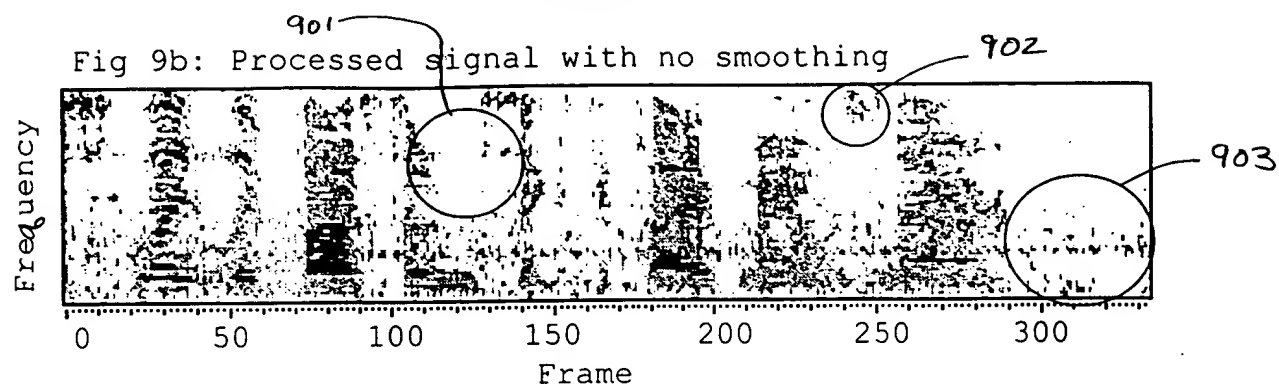
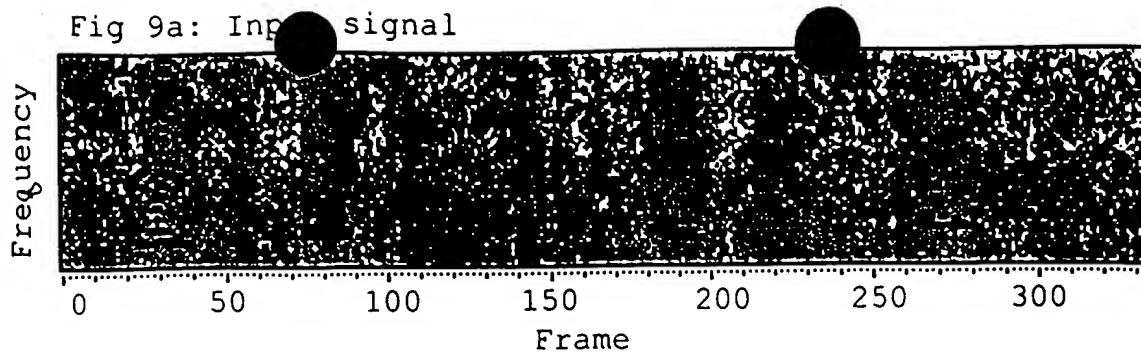


Fig 6: A Typical Weighting Function

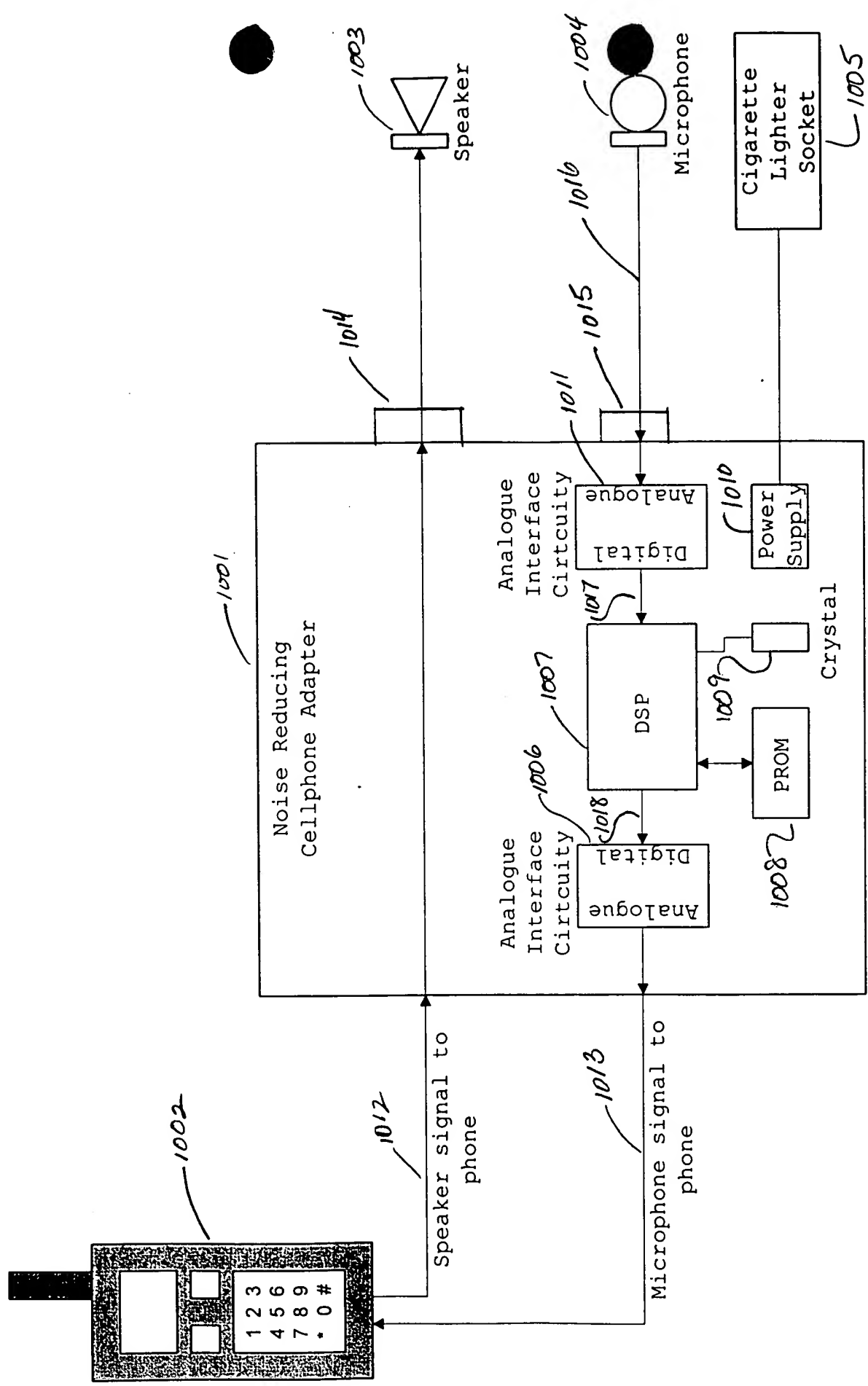








any other type of adapter in any other type of phone



- Fig 10 -